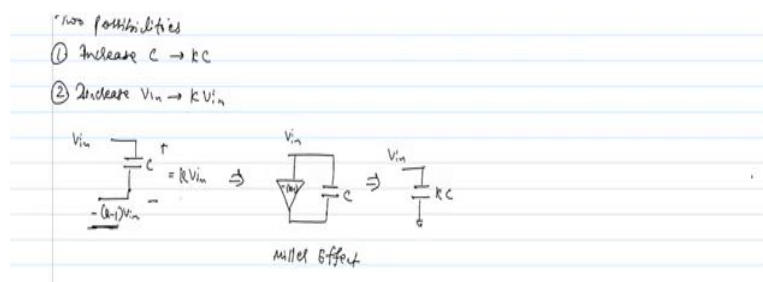


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Lecture - 20
Dominant Pole Compensation using Miller Effect,
RHP Zero due to Miller Capacitor

- Miller compensation: a capacitor is placed in feedback around an inverting amplifier.
- The capacitance (looking in at the input of the inverting amplifier) is Miller multiplied by $1 + \text{gain}$ (of the amplifier). This is known as the Miller effect.



- A common-source amplifier (which is inverting and has a gain of $-g_m R_{out}$) with a capacitor in feedback can be used for Miller compensation.

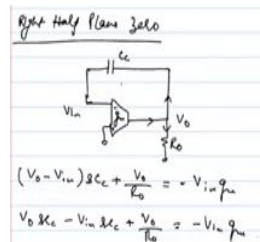
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If $g_m R_o$ is large and C_c is quite small
 Assume previous case, C_c required was 5nF
 $g_m R_o = 300$
 $C_c + C_{in} = \frac{5nF}{300} = 16.67pF$
 $C_c = 16.67pF - 6.4 = 10.27pF$

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- The advantage of Miller compensation is that the capacitance required (for a given location of the dominant pole) is much smaller; it is smaller by a factor of $1 + \text{gain}$.
- The Miller multiplication factor is a function of the output-stage gain, which depends on the load current. One must make sure that the minimum required phase margin is satisfied over the entire range of variation of the load current. The lowest value of the dominant pole frequency occurs at light load.

- Dominant pole compensation requires that there be only a single (dominant) pole below the unity loop gain frequency, to ensure sufficient phase margin. All the other (non-dominant) poles must lie beyond the unity loop gain frequency.
- Miller compensation has the added advantage of pole-splitting, i.e. in addition to the dominant pole moving to a lower frequency, the non-dominant pole moves to a higher frequency, which further reduces the value of the compensation capacitance required.
- Miller compensation introduces a right-half-plane zero, which degrades the phase margin. It occurs at g_m / C_c in respect of the figure shown below.



$$V_o \left(\frac{1}{r_o} + sC_c \right) = V_{in} (sC_c - g_m)$$

$$\frac{V_o}{V_{in}} = \frac{sC_c - g_m}{\frac{1}{r_o} + sC_c}$$

$$z_{RHP} = \frac{g_m}{C_c} \text{ R.H.P.}$$

- One way to mitigate the effect of the RHP zero is to use a nulling resistor in series with the Miller capacitor, as shown in the figure below. If the nulling resistance $R_c > 1/g_m$, then the RHP zero turns into an LHP zero at a frequency $1 / ((R_c - 1/g_m) * C_c)$.

